

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

8-5
D. Hayer
9/26/02



Applicant: Hronik, Stanley A.
Assignee: Integrated Device Technology, Inc.
Title: Double Data Rate Synchronous SRAM with 100% Bus Utilization
Serial No.: 09/347,106 Filing Date: July 2, 1999
Examiner: Anderson, Matthew D. Group Art Unit: 2186
Docket No.: M-7086 US

San Francisco, California
September 19, 2002

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Technology Center 2100

RESPONSE TO OFFICE ACTION

Dear Sir:

This paper is responsive to the non-final Office Action dated June 21, 2002.

Applicant respectfully requests reconsideration of the Application in view of the following amendments and remarks. Please amend the application as follows.

IN THE CLAIMS

Please amend claim 46. In accordance with 37 C.F.R. §1.121(c)(1)(ii), Attachment A provides a marked up version of amended claim 46. For the Examiner's convenience, a clean copy of all pending claims is presented below, beginning on the following page.

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